

Fig. 1

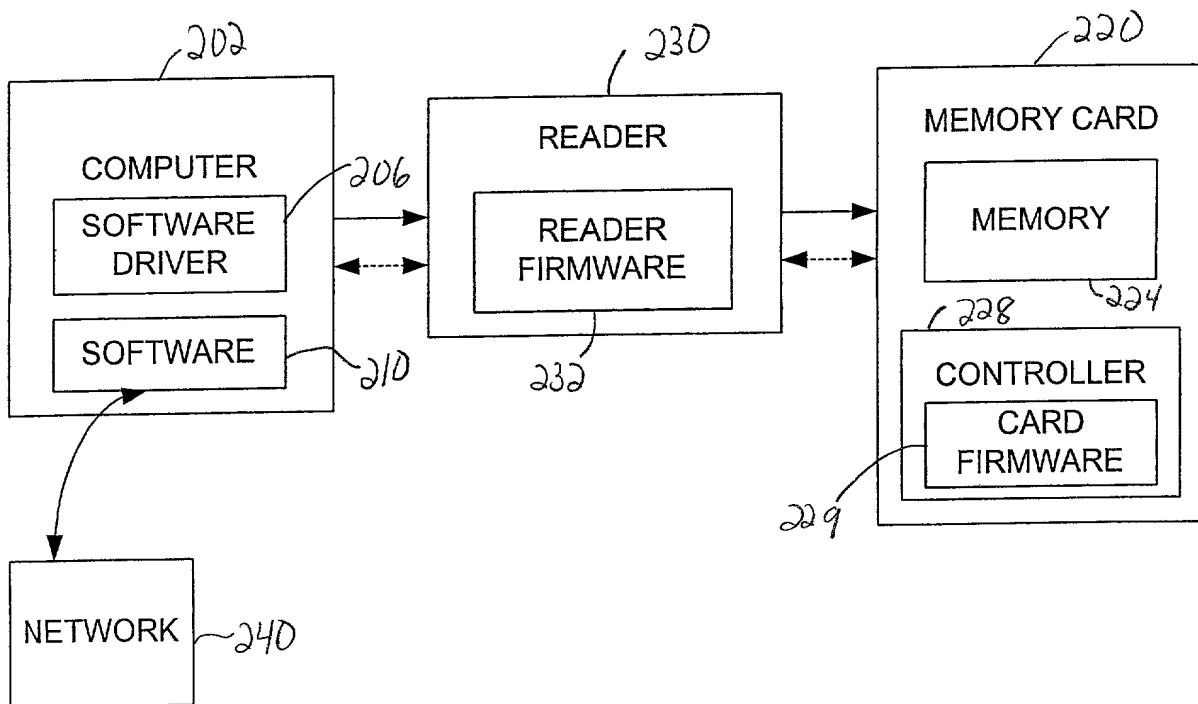


Fig. 2

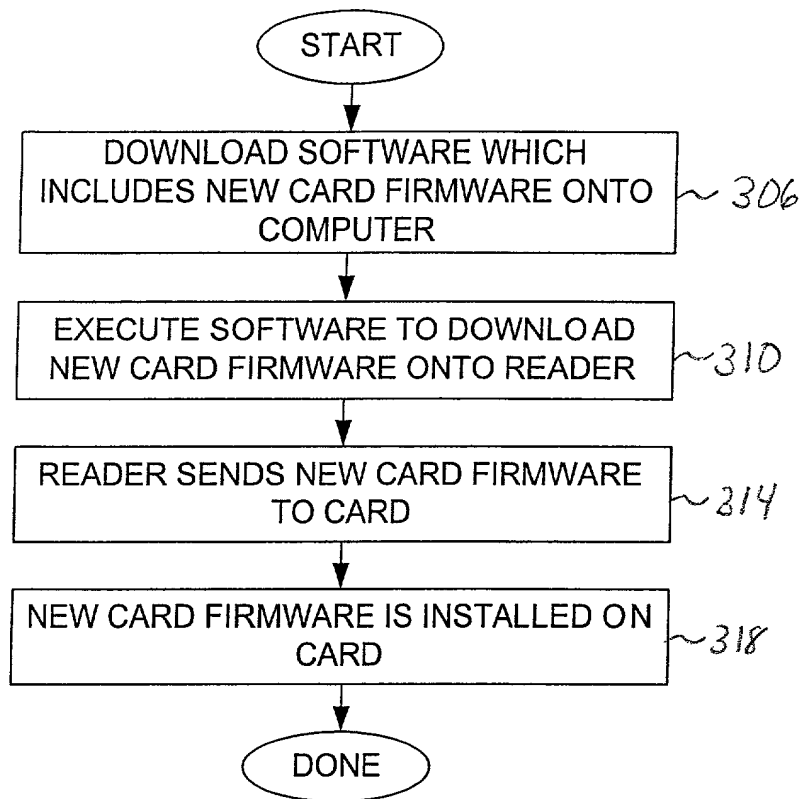


Fig. 3

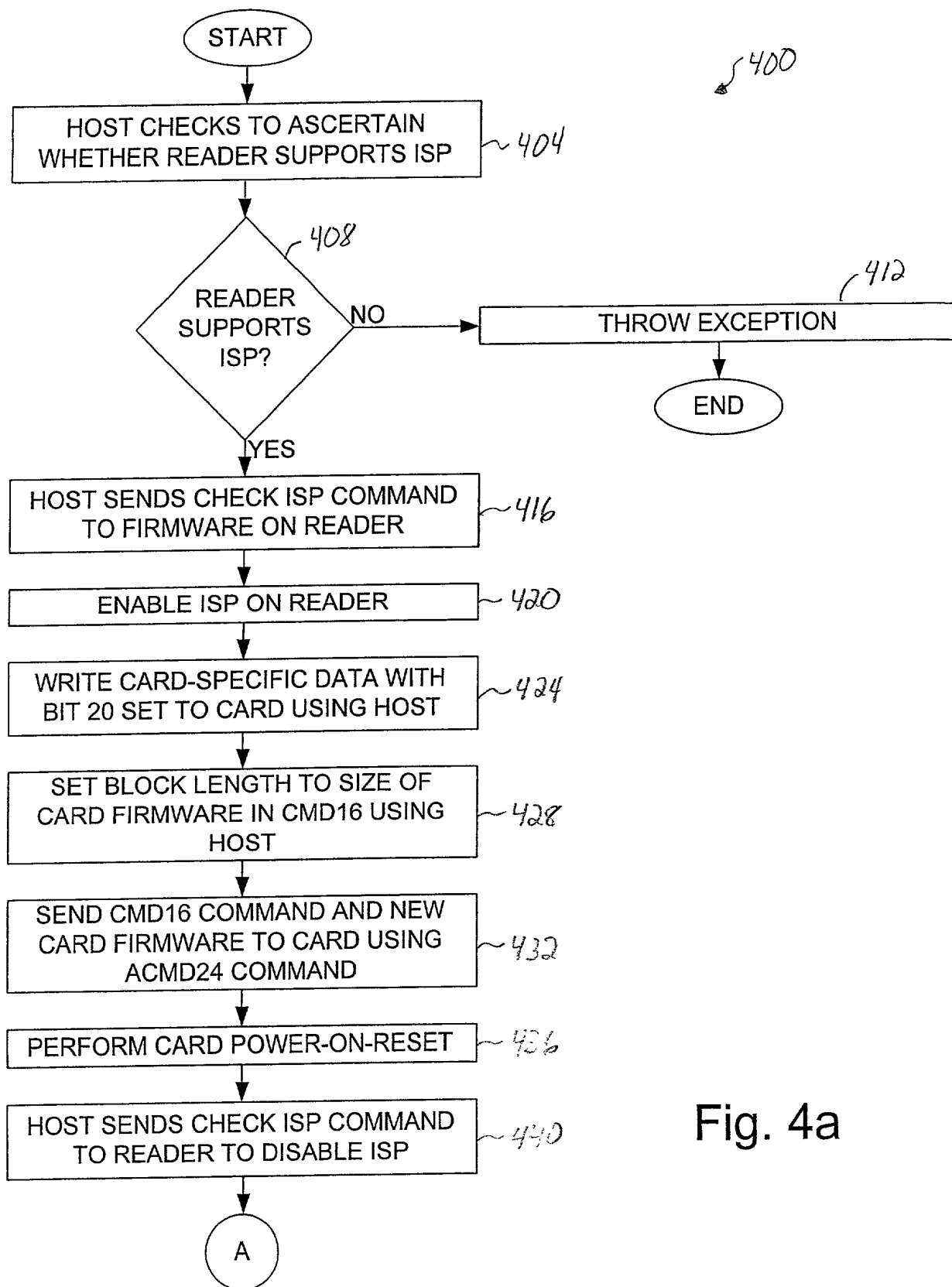


Fig. 4a

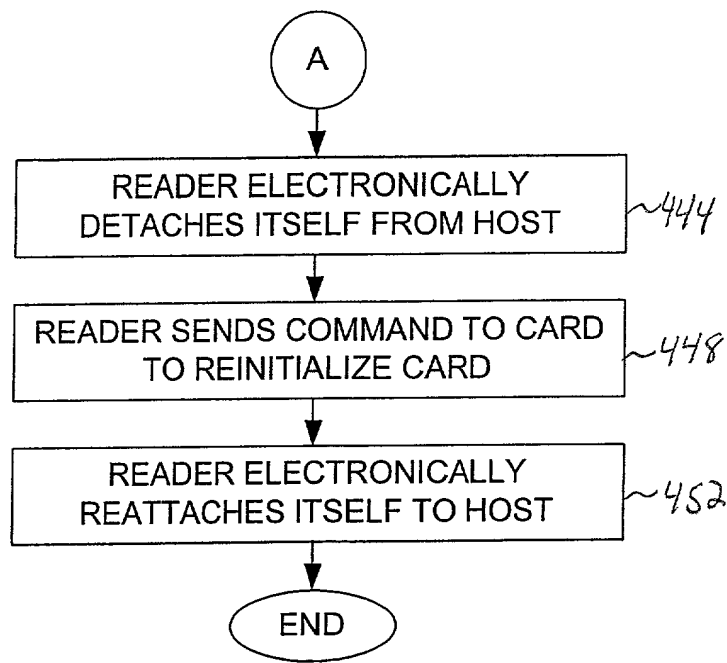


Fig. 4b

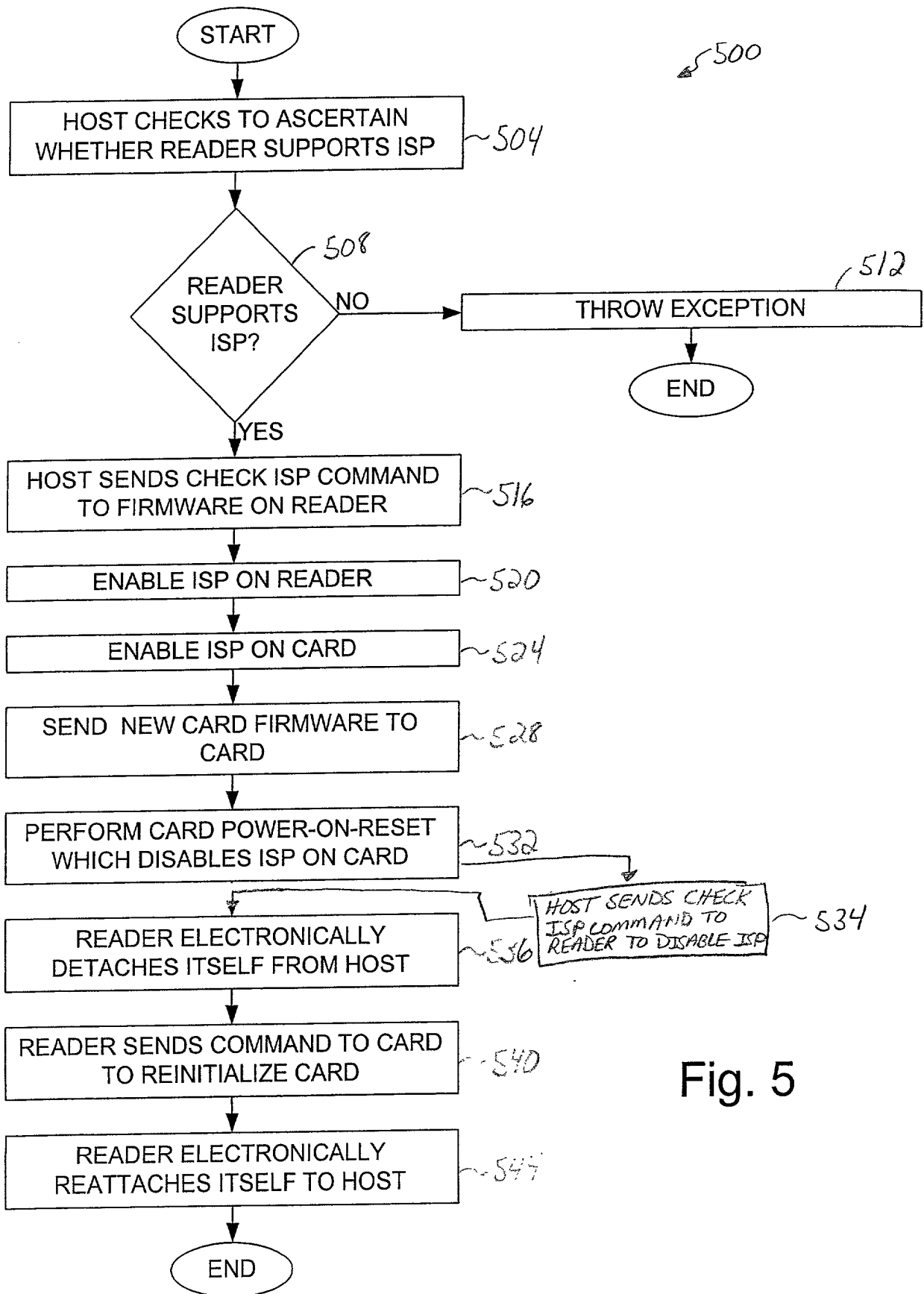


Fig. 5

FIG. 6a

| | Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|-----|------|----------------------|----------|---|---|---|---|---------|----------|
| | Byte | | | | | | | | |
| 60h | 0 | Operation Code (D0h) | | | | | | | |
| 60h | 1 | Reserved | | | | | | | |
| 60h | 2 | Reserved | | | | | | | |
| 60h | 3 | Reserved | | | | | | | |
| 60h | 4 | Reserved | | | | | | | |
| 60h | 5 | Reserved | | | | | | | |
| 60h | 6 | Reserved | | | | | | | |
| 60h | 7 | Reserved | | | | | | | |
| 60h | 8 | Reserved | | | | | | | |
| 60h | 9 | Reserved | | | | | | | |
| 60h | 10 | Lock | Reserved | | | | | ISP ENB | PASS ENB |
| 60h | 11 | Reserved | | | | | | | |

Fig. 6a

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Fig. 6b

| Bit | 660h 660g 660f 660e 660d 660c 660b 660a | | | | | | | |
|-----|---|---|---|---|-----|-----|------|----------|
| | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | Operation Code (D1h) | | | | | | | |
| 1 | Reserved | | | | | | | |
| 2 | Command Index | | | | | | | |
| 3 | Command Argument 1 | | | | | | | |
| 4 | Command Argument 2 | | | | | | | |
| 5 | Command Argument 3 | | | | | | | |
| 6 | Command Argument 4 | | | | | | | |
| 7 | Data Transfer Length (MLSB) | | | | | | | |
| 8 | Data Transfer Length (LMSB) | | | | | | | |
| 9 | Data Transfer Length (LLSB) | | | | | | | |
| 10 | Response Type | | | | DIR | APP | RESP | RESERVED |
| 11 | Control | | | | | | | |

Fig. 6c

652

| | | | | | | | | | |
|------|------|--|---|---|---|---|------|-----|------|
| | Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | Byte | | | | | | | | |
| 706a | 0 | Operation Code (D2h) | | | | | | | |
| 706b | 1 | Reserved | | | | | | | |
| 706c | 2 | 'S' | | | | | | | |
| 706d | 3 | 'D' | | | | | | | |
| 706e | 4 | Reserved (Error Register) | | | | | | | |
| 706f | 5 | Sector Count Register/Feature Register | | | | | | | |
| 706g | 6 | Sector Register | | | | | | | |
| 706h | 7 | Cylinder Low Register | | | | | | | |
| 706i | 8 | Cylinder High Register | | | | | | | |
| 706j | 9 | Device/Head Register | | | | | | | |
| 706k | 10 | Command/Status Register | | | | | | | |
| 706l | 11 | Reserved | | | | | DATA | DIR | REGS |

Fig. 7a

752

707

| | | | | | | | | | |
|------|------|--|---|---|---|---|---|---|---|
| | Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | Byte | | | | | | | | |
| 716a | 0 | 'S' | | | | | | | |
| 716b | 1 | 'D' | | | | | | | |
| 716c | 2 | Reserved (Error Register) | | | | | | | |
| 716d | 3 | Sector Count Register/Feature Register | | | | | | | |
| 716e | 4 | Sector Register | | | | | | | |
| 716f | 5 | Cylinder Low Register | | | | | | | |
| 716g | 6 | Cylinder High Register | | | | | | | |
| 716h | 7 | Device/Head Register | | | | | | | |
| 716i | 8 | Command/Status Register | | | | | | | |

Fig. 7b

| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|---------------|-----|----|-----|-----|----|---|---|-----|
| Feature | na | | | | | | | ENB |
| Sector Count | na | | | | | | | |
| Sector Number | na | | | | | | | |
| Cylinder Low | na | | | | | | | |
| Cylinder High | na | | | | | | | |
| Device/Head | obs | na | Obs | DEV | na | | | |
| Command | DSH | | | | | | | |

Fig. 8a

| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|---------------|-----|------|-----|-----|-----|------------|---|-----|
| Error | 0h | | | | | | | |
| Sector Count | 55h | | | | | | | |
| Sector Number | AAh | | | | | | | |
| Cylinder Low | 0h | | | | | | | |
| Cylinder High | 0h | | | | | | | |
| Device/Head | obs | na | Obs | DEV | WP | Media Type | | |
| Status | BSY | DRDY | DF | 1 | DRQ | 0 | 0 | ERR |

Fig. 8b

| | | | | | | | | |
|---------------|------|------|------|------|------|------|------|------|
| | 826h | 826g | 826f | 826e | 826d | 826c | 826b | 826a |
| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| Error | 0 | 0 | 0 | 0 | 0 | ABRT | 0 | 0 |
| Sector Count | na | | | | | | | |
| Sector Number | na | | | | | | | |
| Cylinder Low | na | | | | | | | |
| Cylinder High | na | | | | | | | |
| Device/Head | obs | na | Obs | DEV | na | | | |
| Status | BSY | DRDY | DF | na | DRQ | na | na | ERR |

832f 832g

Fig. 8c

| | | | | | | | | | |
|---------------|--|------|------|------|------|------|------|------|------|
| | | 848h | 848g | 848f | 848e | 848d | 848c | 848b | 848a |
| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | |
| Feature | 0h | | | | | | | DIR | |
| Sector Count | Number of sectors | | | | | | | | |
| Sector Number | Starting LBA of Firmware (LSB) | | | | | | | | |
| Cylinder Low | Starting LBA of Firmware (Middle Byte) | | | | | | | | |
| Cylinder High | Starting LBA of Firmware (MSB) | | | | | | | | |
| Device/Head | obs | na | obs | DEV | 0h | | | | |
| Command | D6h | | | | | | | | |

852f 852g 844

Fig. 8d

| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|---------------|-----|------|-----|-----|-----|----|----|-----|
| Error | na | | | | | | | |
| Sector Count | na | | | | | | | |
| Sector Number | na | | | | | | | |
| Cylinder Low | na | | | | | | | |
| Cylinder High | na | | | | | | | |
| Device/Head | obs | na | Obs | DEV | na | | | |
| Status | BSY | DRDY | DF | na | DRQ | na | na | ERR |

872a, 872b, 872c, 872d, 872e, 872f, 872g, 864

Fig. 8e

| Register | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|---------------|-------------------------|------|-----|-----|-----|------|----|-----|
| Error | na | UNC | na | na | na | ABRT | na | na |
| Sector Count | na | | | | | | | |
| Sector Number | Error LBA (LSB) | | | | | | | |
| Cylinder Low | Error LBA (Middle Byte) | | | | | | | |
| Cylinder High | Error LBA (MSB) | | | | | | | |
| Device/Head | obs | na | obs | DEV | 0h | | | |
| STATUS | BSY | DRDY | DF | na | DRQ | na | na | ERR |

884a, 888h, 888g, 888f, 888e, 888d, 888c, 888b, 888a, 892a, 892b, 892c, 892d, 892e, 892f, 892g, 892h

Fig. 8f